

# Frequency Synthesizer Module, Low Phase Noise, U-Band

**SOT-40360320200-19-E6** is a low phase noise frequency synthesizer module designed and manufactured for standard test instrumentation. communication, and radar systems as a local oscillator. The module covers a frequency range of 40 to 60 GHz with an exceptional low harmonics and spurious emissions as well as superior low phase noise performance. The model is externally referenced. The output power of the module +20 dBm with 3 dB power flatness. The frequency step resolution of the module is up to 0.4 Hz. The phase noise of the oscillator is dependent on the quality of the reference source. The oscillator has a built-in voltage regulator to further improve the signal quality and provide over voltage protection. The normal operating state of the oscillator is external referenced. This module can be directly controlled with digital signals following SPI communication protocol through Micro-D connector. Eravant offers an evaluation kit including the control board under model number, SOT-EVA-S1, with the GUI (Graphic User Interface) for initial system set and rapid system development.



# **Electrical Specifications:**

Parameter	Minimum	Typical	Maximum
Output Frequency Range	40 GHz	MILLIM	60 GHz
Step Size		0.4 Hz	
Output Power		+20 dBm	
Output Power Flatness	3 dB		
Frequency Stability	Same as External Reference		
Frequency Accuracy	Same as External Reference		
Output Spurious		-50 dBc	
Output Harmonics		-15 dBc	
External Reference	100 MHz/ +5 dBm ± 3 dBm		
Lock Indicator (LD)	TTL High		
Phase Noise	REF Phase Noise + (20Log(N)+6) dBc/Hz*		
Frequency Switching Time	≤200 µs (Excludes the Series Port Communication Time.)		
Control Interface	SPI		
Synthesizer (Bias)		+12 V <sub>DC</sub> /900 mA	
Frequency Multiplier (Bias)		+8 V <sub>DC</sub> /1,000 mA	
Specification Temperature		+25 °C	
Operating Temperature	0 °C		+50 °C
* N is the frequency multiplication f	factor.		

### **ECCN**

EAR99

### **FEATURES**

- Low Phase Noise and Harmonics
- · External Referenced
- SPI Communication Port

## **APPLICATIONS**

- Radar Systems
- · Communication Systems
- Test Instrumentations

### **SUPPLEMENTAL DETAILS**



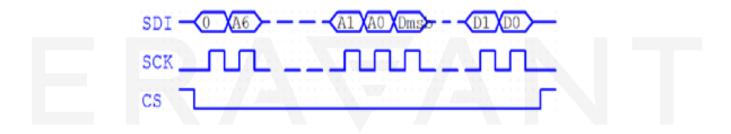
# **Mechanical Specifications:**

Item	Specification	
RF Port	WR-19 Waveguide with UG383/U-M Anti-Cocking Flange	
Bias, Control Port and Lock Indicator (Synthesizer)	9 Position D-Type, Micro-D Socket (Female)	
Bias (Frequency Multiplier)	Solder Pin	
External Reference Input Port	SMA (F) Connector	
Body Material	Aluminum	
Finish	Black Anodized	
Dimension	5.50" (L) x 3.55" (W) x 1.10" (H)	
Outline	OT-EU-A-FM	

# System Block Diagram:



# **SPI Communication Port:**



The device is controlled with a SPI communication port. Digital high level (TTL High) is +3.3 V, and its effective range is from +2.7 V to +3.6 V: digital low level (TTL Low) is 0 V and its effective range is from 0 to +0.6 V. The send control signal to this module, the W/R bit is first sent, followed by register address, and data.



# W/R Control Register Bit:

High-Order Bit (W/R)	Definition	Note
0	Write	-
1	Read	Reserved

# **Register Map and Bit Descriptions:**

Register Address	Bit Range	Description	Default Value
0x02	[63:16]	Synthesized Frequency	0x00174876E800
	[15:0]	Reserved	0x0000

The unit of synthesized frequency is 0.1 Hz before x4 multiplication. For example, when frequency is synthesized to 5 Ghz, the data in bits [63:16] is 50000000000. The data sent through SPI is 0x02 000BA43B7400 0000. The default synthesized frequency is 10 GHz.

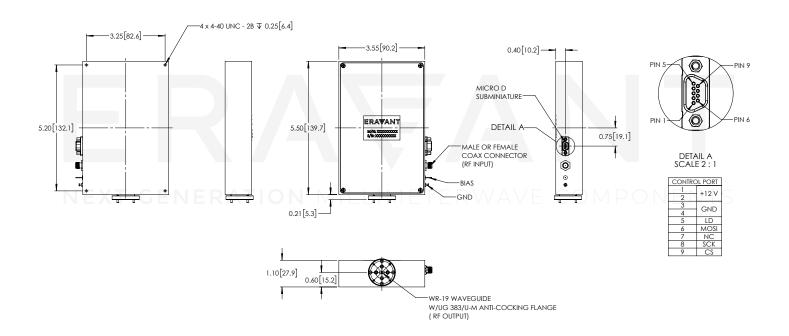
# **Evaluation Kit:**

The evaluation kit includes a control board, <u>SOT-EVA-S1</u>, and GUI. The control board is not included in this product, but it can be ordered separately. The evaluation kit allows the users to easily set up the system and control the synthesized frequency of this module manually. The computer display of the GUI interface is shown below.



### **Mechanical Outline:**

Unless otherwise specified, all dimensions are in inches [millimeters])



### NOTE:

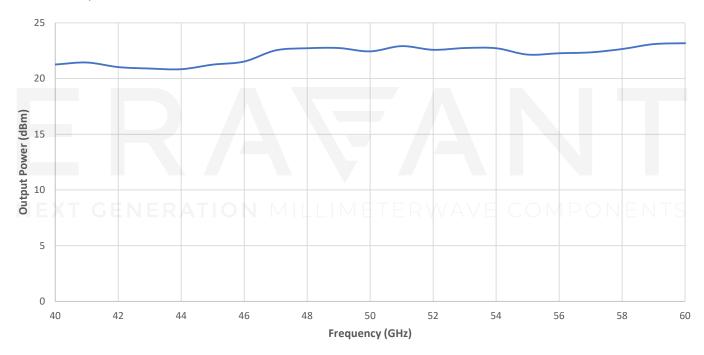
- · All data presented is collected from a sample lot. Actual data may vary unit to unit, slightly.
- All testing was performed under +25 °C case temperature.
- Well-regulated DC power supplies capable of delivering +12 V<sub>DC</sub>/1.5 A is required. The proper connection of the power supply to the Micro-D connector is illustrated in the outline drawing above.
- Standard Micro-D9 male to Micro-D9 male cable SOT-CC-M9MM9M-S1 is provided to connect synthesizer with Evaluation Kit <u>SOT-EVA-S1</u>.
- The device is controlled via Personal Computer. A SPI-to-serial adapter is needed between the device and PC.
- Eravant reserves the right to change the information presented without notice.

## **CAUTION:**

- The device is static sensitive. Always follow ESD rules when working with the device.
- Wrong bias or reverse bias on the sensor will damage the device.
- Exceeding absolute maximum ratings shown will damage the device. Use additional heatsink or fan
  if necessary. The case temperature of the device shall never exceed +75 °C.
- Proper torque, 8.0 ± 0.15 inch-pounds (0.90 ± 0.02 Nm), should be applied. Eravant torque wrench, model <u>SCH-08008-S1</u>, is highly recommended.

# **Typical Output Power vs. Frequency**

Bias: +12Vdc/ 2.3A



# ERAFANT

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